

Voltage control in distribution networks with high DER integration: HIL experiences

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Overview

- Overview of the voltage rise issue at distribution networks
- Testing local voltage control approaches: Power Hardware in the Loop (PHIL) testing



 Testing coordinated voltage control approaches: Controller Hardware in the Loop (CHIL) testing



Transition to tomorrow's grids





Voltage rise due to DG integration



PV integration in Germany

- Around 40 GW of PV systems in Germany. MV \approx 13 GW, LV \approx 22 GW
- Study at large DSOs^{1.} Main problems due to PV integration to the LV network:
 - Difficulty to maintain the voltage inside the desired range (± 10%)
 - Thermal overloading of equipment (MV/LV transformers and lines)
- Solutions to the voltage rise problem:
 - o Grid reinforcement
 - o HV/MV transformer with OLTC
 - Absorption of reactive power by the DG
 - Active power curtailment by the DG
 - Use of storage systems
- Power electronic interfaces:
 - Constant cos p
 - Cos ϕ (P) characteristic

Constant Q Q(V) characteristic



$$\Delta V \approx \frac{P_{cp} \cdot R + Q_{cp} \cdot X}{V_G}$$

Voltage support by Distributed Generation

Local control:

- <u>cosφ(P) characteristic:</u>
 - Usually voltage rise occurs at high PV power
 - Excessive absorption of reactive power
- Q(V) characteristic
- P(V) characteristic:
 - PV active power curtail (if Q cannot solve the problem)
 - LV networks (R>X)

Secondary control :

The inverters receive set-points (P-Q set-points) from the DSO

BDEW: MV, Germany , 2008 CEI 0-21: LV, Italy, 2011 CLC/TS 50549-1: LV, EU, 2015 E-control-TOR D4: MV, LV Austria 2016 VDE-AR-N 4105: LV, Germany, 2011 EN 50438: LV, ≤16 A, EU, 2013 CLC/TS 50549-2: MV, EU, 2015 IEEE 1547: USA, 2018



PHIL test at the CIGRE Bechmark LV microgrid

- Simulated and hardware DGs operate with Q(V) droop control
- Load is reduced: Q absorption results in voltages closer to the nominal value
- The hardware PV inverter operates near its nominal operating point, so it reduces
 P in order to absorb Q (according to the Q(V) droop)
- Fluctuations occur on the voltages



(HuT)

A 0



Transition from grid-connected to island mode – PHIL testing

- Storage system: f (P), V (Q) droop curves in island mode
- DG units: *P* (*f*) and *Q* (*V*) droop curves according to standards (grid-connected)

- Excess of active power: frequency rises
- When the DG units operate with P (f) droop control, they decrease their active power, leading to improved frequency response



P. Kotsampopoulos, D. Lagos, N. Hatziargyriou, M.O. Faruque et al. "A Benchmark System for Hardware-in-the-Loop Testing of Distributed Energy Resources", IEEE Power and Energy Technology Systems Journal, 2018 8

Transition from grid-connected to island mode - PHIL testing



- Storage system: provides $Q \rightarrow$ voltage is reduced according to the V(Q) droop
- The voltage reduction is mitigated by the reactive power provision of the DGs, according to their Q(U) characteristics



PHIL tests on the interaction between DG and OLTC control



- Commercial PV inverter of lower nominal power with the same capabilities of voltage control: Q(V), cosφ(P)
- The nominal power of the inverter is virtually increased in the DRTS

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Joint work with the Austrian Institute of Technology - AIT

*P. Kotsampopoulos, F. Lehfuss, G. Lauss, B. Bletterie, N. Hatziargyriou, "The limitations of digital simulation and the advantages of PHIL testing in studying Distributed Generation provision of ancillary services", IEEE Transactions on*₁₀ *Industrial Electronics, 2015*

DG and OLTC interactions: cos $\phi(P)$ control

- DG active power increases, stays constant and then decreases
- Recurring tap-changes occur
- Good accuracy of the PHIL test





DG and OLTC interactions: Q(V) control

- Active power of the DG increases → DG voltage increases → reactive power absorption by the DG increases (Q(V)) → Voltage of the secondary of the transformer decreases → tap-change occurs
- Recurring tap-changes occur
- Instability of the Q(V) controller (i.e. Oscillations): not visible at the pure digital simulation



DG and OLTC interactions: Q(V) control

- Voltage drop at the HV network
- Similarly, the oscillations are not shown at the pure-digital simulation



Centralized Coordinated Voltage Control (CVC)

- Optimal solution to voltage rise (due to high DG penetration) and voltage drop (during peak load periods) problems
- Optimization problem: Mixed Integer Non-Linear Programming (MINLP)
- Inputs:
 - Load active and reactive power
 - PV active power
 - Battery SoC
 - Current Tap position
- Outputs (operational set-points):
 - Battery active and reactive power
 - PV reactive power
 - New Tap position
- Battery Management



M. Maniatopoulos, D. Lagos, P. Kotsampopoulos, N. Hatziargyriou, "Combined Control and Power Hardware-in-the-Loop simulation for testing Smart grid control algorithms", IET Generation, Transmission & Distribution, 2017

Coordinated Voltage Control – Optimization Problem Formulation

$$\min_{x} f(x) = w_1 * \sum_{i=1}^{12} \sum_{j=1}^{12} P_{losses,ij} + w_2 * \sum_{k=1}^{6} (V_k - 1)^2 + w_3 * |tap_{new} - tap_{current}|$$

$$x = [V_1 \dots V_{12} \quad \delta_1 \dots \quad \delta_{12} \quad P_{bat} \quad Q_{bat} \quad Q_{pv,1} \quad Q_{pv,2} \quad Q_{pv,3} \quad Q_{pv,4} \quad Tap_changes]$$

$$P_{losses,ij} = -G_{ij}[V_i^2 + V_j^2 - 2V_iV_j \cos \delta_{ij}]$$

$$tap_{new} = Tap_reference + Tap_changes$$

$$w_1, w_2, w_3 = \text{weights for the objective function terms}$$

Constraints:

Supervisory Controller testing: Proposed testing chain of Smart Grid control algorithms



CVC validation: Laboratory Setup



The CVC algorithm was tested in pure simulation, CHIL and finally combined CHIL and PHIL.

- The combined CHIL and PHIL setup also provided:
 - Insight on communication issues between the controller and the real hardware
 - Validation of the CVC with real power hardware behaviour

CVC Results CHIL & PHIL simulation



Voltage of all nodes without voltage control

Voltage of all nodes with CVC

CVC Results CHIL & PHIL simulation



- The SoC of the BESS was restored to the reference level of 40% during the night to early morning hours (12 a.m. to 9 a.m.), so that it is available for maximum charging during the midday hours of high irradiance.
- The active power exchange of the BESS was restricted to periods of either high irradiance (charge) or high load demand (discharge), where the voltage rise/drop problems are greatest.

CVC Results CHIL & PHIL simulation



• The PV inverters contributed to voltage control by either absorbing (during hours of high irradiance to reduce the voltages) or generating (when an increase of the voltage is required) reactive power.

Remote real-time simulation via OPSim

- > **OPSim tool**: developed by Fraunhofer IEE
- Interconnect two geographically distributed simulators via the co-simulation environment OPSim to assess delay impact on Real-Time Simulation and to understand the boundaries in the co-simulation environment OPSim.
- Determine a holistic performance of a Coordinated Voltage Control (CVC) algorithm.





J. Montoya, R. Brandl, M. Vogt, F. Marten, A. Fabian, M. Maniatopoulos, "Asynchronous Integration of a Real-Time Simulator to a Geographically Distributed Controller through a Co-Simulation Environment", IECON 2018

Remote real-time simulation: Test results



IEE



Conclusions

- Voltage rise due to DG integration is and will be a "hot topic" for the DSOs
- The flexibility of DER can be used to support the voltage
- HIL is a system-level testing method that proves to be beneficial for testing voltage control approaches in realistic and flexible conditions
- The PHIL tests of local voltage control revealed oscillations that were not visible in pure digital simulation
- The coordinated voltage control scheme was tested effectively in realistic conditions using the combined CHIL and PHIL environment
- A testing chain for system controllers (DMS/EMS) was proposed: combined CHIL and PHIL approach is more realistic.
- Remote real-time simulation test: the delays can impact the operation of the control algorithm



Thank you for your attention!

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